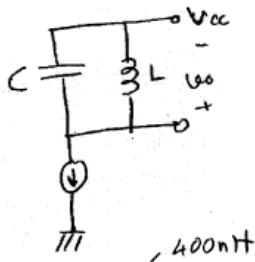


**Question 1) (20 points)**



The tank circuit which is shown above is to be used at a high frequency design. The self resonance of the inductor is 100 MHz. Please answer the following:

- (10 points) Can you make a 200 MHz resonant tank circuit using this inductor? If so, calculate the value of the capacitor to obtain the resonance frequency, ~~explain your answer~~.
- (10 points) Can you make a 50 MHz resonant tank circuit using this inductor? If so, calculate the value of the capacitor to obtain the resonance frequency, ~~explain your answer~~.

a-) No, because the inductor acts as a capacitor above its parallel resonance frequency.

b-) Yes, the equivalent model of the inductor is  $\omega_0$  at self resonance is  $628.3 \times 10^6$  rad/sec

$$L = 400 \text{ nH} \quad \boxed{\frac{1}{C_S}} = C_S = \frac{1}{\omega_0^2 L} = \frac{1}{(2\pi \times 100 \times 10^6)^2 \times 400 \times 10^{-9}} \\ = 6.333 \times 10^{-12} \text{ F}$$

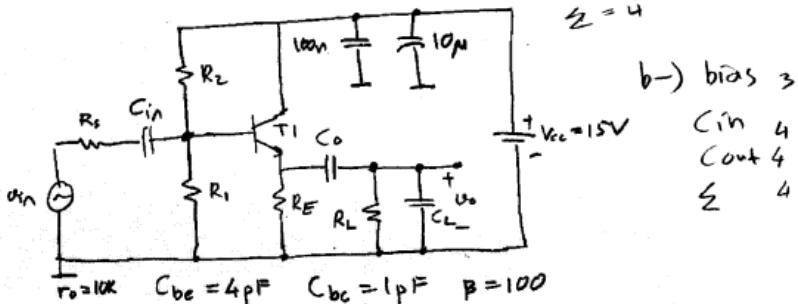
Four times  $C_L$  is needed to lower the parallel resonance to its half  $\Rightarrow$  The total parallel capacitance

$$C_T = 25.33 \text{ pF}$$

The capacitance needed over the  $C_S$  is

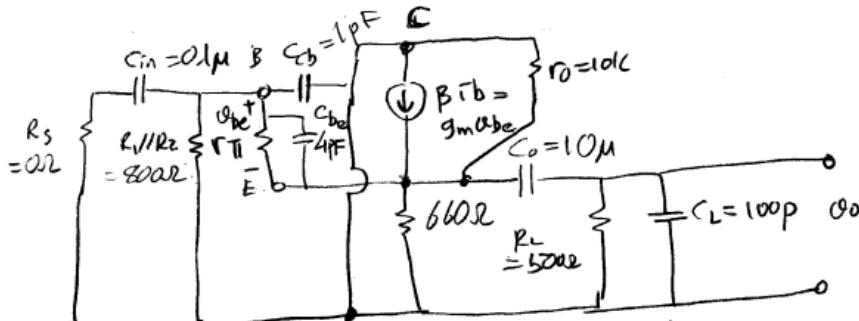
$$25.33 - 6.33 = 19 \text{ pF}$$

**Question 2) (30 points)**



For the circuit given above,  $R_s = 0\Omega$ ,  $R_1 = 1k\Omega$ ,  $R_2 = 4k\Omega$ ,  $R_E = 660\Omega$ ,  $V_T = 0.7V$ ,  $C_{in} = 100nF$ ,  $C_{be} = 10\mu F$ ,  $R_L = 500\Omega$  and  $C_L = 100pF$ . Assume that  $R_T \neq R_2 + R_S$ .

- (15 points) Estimate the higher 3 dB cut-off frequency by the method of open-circuit time constants.
- (15 points) Estimate the lower 3 dB cut-off frequency by the method of short-circuit time constants.



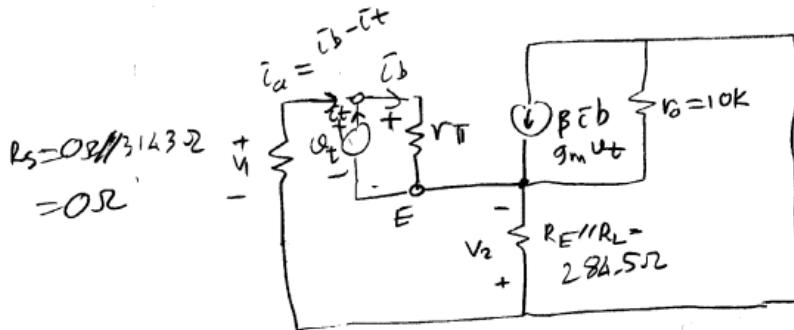
a-) At high frequencies  $C_{in}$  &  $C_o$  are short. To calculate  $R_{be}$ , open circuit the high-frequency capacitors,

and put  $V_T$  across B and E

$$I_{BE} = \left( 15 \times \frac{1}{1+4} - V_T \right) \left( 160 \parallel \frac{1 \times 4}{5(B+1)} \right) = \frac{3-0.7}{660+8} = \frac{2.3V}{668\Omega} = 3.44mA$$

$$= 3.44mA$$

$$\frac{1}{gm} = \frac{26}{3.44} = 7.56\Omega \quad r_{\pi} = \frac{26}{7.56} = \frac{B}{gm}$$



$$R'_E = R_E // R_L // r_o = 276.6 \Omega$$

$$i_a = i_b - i_t \quad v_t = i_b r_{\pi} = v_1 + v_2 = -i_a R_s - (\beta + 1) i_b R_E' + i_t R_E'$$

$$i_t = i_b r_{\pi}' = -i_b R_s + i_b R_s - (\beta + 1) i_b R_E' + i_t R_E'$$

$$i_b = (r_{\pi} + R_s + (\beta + 1) R_E' - (R_s + R_E')) i_t \Rightarrow i_b = \frac{R_s R_E'}{r_{\pi} + R_s + (\beta + 1) R_E'} i_t$$

$$R_E' = \frac{i_t}{i_t} = \frac{(R_s + R_E') r_{\pi}}{r_{\pi} + R_s + (\beta + 1) R_E'} \quad \frac{i_E}{i_E} = \frac{276.6 \times 756}{756 + 101 \times 276.6} = 7.3 \Omega$$

$$\tau_{be} = 7.3 \times 4 \times 10^{-12} = 29.2 \text{ psec} = 0.029 \text{ nsec}$$

R<sub>cb</sub> is a capacitor connected between base & the ground.  
Therefore it sees the amplifier input impedance in parallel with R<sub>s</sub>  $\Rightarrow 29.1 \text{ psec}$

$$R_{cb} = \underbrace{R_s // R_1 // R_2 // (r_{\pi} + (\beta + 1)(R_E' // R_L // r_o))}_{\text{or}}$$

$$\Rightarrow R_{cb} = 0 // (756.8 + 101 \times 276.6) \underset{\text{... large compared to } 0 \Omega}{=} 0 \Omega$$

$$\tau_{bc} = 0 \text{ sec}$$

(2-a) continued

$C_L$  sees  $R_{CL}$

$$R_{CL} = r_o // R_L // R_E // \left( \frac{r_T}{(\beta+1)} + \frac{R_s // R_1 // R_2}{\beta+1} \right)$$
$$\approx 1/g_m \quad \approx 0$$
$$= (276.6 \Omega) // \left( \frac{1}{g_m} + \frac{0}{101} \right) = 276.6 // \left( \frac{26}{3.54} + 0 \right)$$

$$= 276.6 // 7.5 = 276.6 // 7.5 \equiv 7.3 \Omega$$

$$\approx \left( \frac{1}{g_m} + \frac{R_s}{\beta+1} \right)$$

$$T_{CL} = 7.3 \Omega \times 100 \times 10^{-10} = 730 \text{ psec} = 0.730 \text{ nsec}$$

$$\sum T = T_{be} + T_{cb} + T_{CL} = 0.023 \text{ nsec} + 0 + 0.73 \text{ nsec}$$
$$0.759 \text{ nsec}$$

$$w_{zdB} = 1317 \text{ Mrad/sec}$$

$$f_{zdB} = 209.7 \text{ MHz}$$

(2-b)

b-)  $R_{Cin} = R_s + \left( r_{\pi} + (\beta+1) \left( R_E // R_0 // R_L \right) \right) // \underbrace{R_1 // R_2}_{800\Omega}$

$$= 0 + (756 + 104 \times 276) // 800 \rightarrow 28692 // 800 = 778\Omega$$

$$R_{Co} = R_E // R_0 // \left( \frac{1}{g_m} + \frac{R_s}{\beta+1} \right) + R_L$$

$$660 // 10000 // (256 + 500) \approx 507.6$$

$$T_{Cin} = 778 \times 10^{-7} = 0.778 \times 10^{-4} \text{ sec}$$

$$T_{Co} = 507.6 \times 10^{-5} \text{ sec}$$

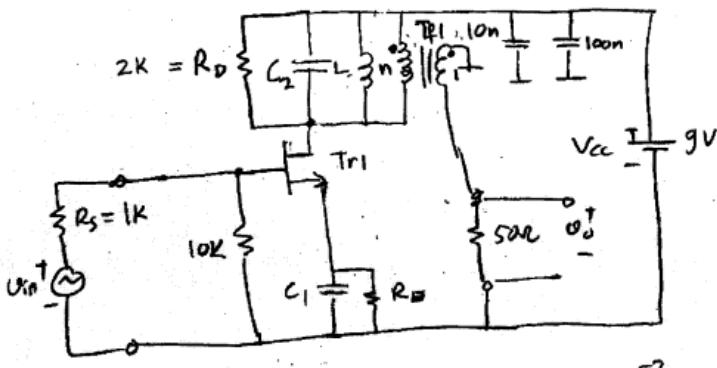
$$\frac{1}{T_{Co}} + \frac{1}{T_{Cin}} = 197 \text{ rad/sec} + 12853 \text{ rad/sec} = 13050 \text{ rad/sec}$$
$$f_{3dB} = 2077 \text{ Hz}$$

2-a) alternative solution

$$R_{Re} = \frac{R_E' r_{\pi}}{r_{\pi} + (\beta+1) R_E'} \quad \text{for } R_s = 0$$

$$= \frac{1}{\frac{r_{\pi}}{r_{\pi} R_E'} + \frac{\beta R_E}{R_E' r_{\pi}} + \frac{R_E'}{R_E' r_{\pi}}} = \frac{1}{\frac{1}{R_E} + g_m + \frac{1}{r_{\pi}}}$$

**Question 3) (26 points)**



At the tuned circuit given above,  $C_{gd}=1\text{pF}$ ,  $C_{gs}=3\text{pF}$ ,  $g_m=10\text{mS}$ ,  $C_1=100\text{nF}$ ,  $R_E=120\Omega$ . Design the tuned circuit by:

- a) (6 points) By finding the transformer ratio,  $n$  ( $T_n$  is an ideal transformer), to present matched impedance to the drain of the tank circuit.
  - b) (6 points) By choosing  $C_2$  such that it is 3 times the reflected miller capacitance to the drain,
  - c) (6 points) By choosing  $L$  to operate at 40 MHz,
  - d) (8 points) Verify your design by calculating the 3dB BW of the gate circuit? Comment on the result.

$$1) n=50 = 2000 \Rightarrow n = \frac{2000}{50} = 40 \Rightarrow n = 6.35$$

$$C_{Miller} = Cgd \times (1 + R_s g_m) = 1 pF \times 11 = 11 pF$$

$$C_2 = 3 \times 11 pF = 33 pF$$

$$c-) C_{\text{total}} = 33 \mu F + 11 \mu F = 44 \mu F$$

$$L_3 = \frac{1}{\omega_0^2 C_{\text{drain}}} = \frac{1}{(2\pi \cdot 40 \times 10^6)^2 \cdot 44 \times 10^{-12}} = 360 \text{ nH}$$

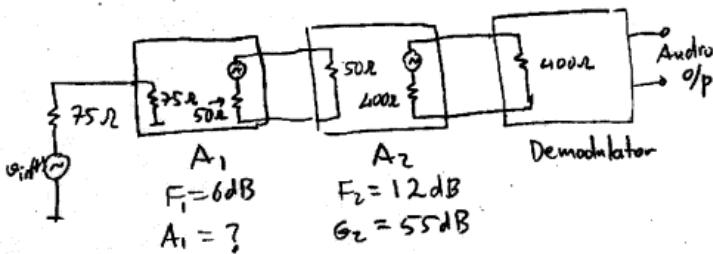
$$d) C_{in} = (1 + R_L g_m) C_{gd} = 1 \text{ pF} (1 + 10) = 11 \text{ pF} + 3 \text{ pF} = 14 \text{ pF}$$

$$W_{3dB} > \frac{1}{\sqrt{-2 \cdot \ln(0.5)}} = 71.43 \text{ Mrad/sec} \Rightarrow f_{3dB} = 11.37 \text{ MHz}$$

T. Rayhan

IEEE4UIMT 2008-11-23-2013

**Question 4) (24 points)**



Two amplifiers and a demodulator is cascaded as shown in the figure given above. Please answer the following:

- (8 points) What should be the minimum gain of A1 so that the overall noise figure of the cascaded amplifier is less than or equal to 7dB.
  - (8 points) Calculate the minimum signal level at the input of the cascaded amplifier in dBm, so that the cascaded amplifier can deliver a S/N ratio =12dB at its output to the demodulator if the bandwidth of the demodulator is 100kHz?
  - (8 points) What is the noise temperature of the cascaded amplifier?
- a-)  $7 \text{dB} \Rightarrow 5 \text{times}, 6 \text{dB} \Rightarrow 4 \text{times}, 12 \text{dB} \Rightarrow 16 \text{times}$
- $$F_{1,2} = 5 = 4 + \frac{F_2 - 1}{G_1} \Rightarrow G_1 = \frac{15}{1} = 15$$
- $$G_1 = 10 \log_{10} 15 = 11.7 \text{dB} \approx 11.7 \text{dB}$$
- b-)  $S_{\text{required}} = F k T_o B \times (S/N_{\text{required}})$   
 $= 5 \times 1.38 \times 10^{-23} \times 290 \times 10^5 \times 16 = 3.2 \times 10^{-14} \text{Watts}$   
 $= 3.2 \times 10^{-11} \text{mW}$   
 $= 10 \log_{10} 3.2 \times 10^{-11} = -104.9 \text{dBm}$

c-)  $(F - 1) 290 = T_n = (5 - 1) \times 290 = 4 \times 290 = 1160 \text{K}$