

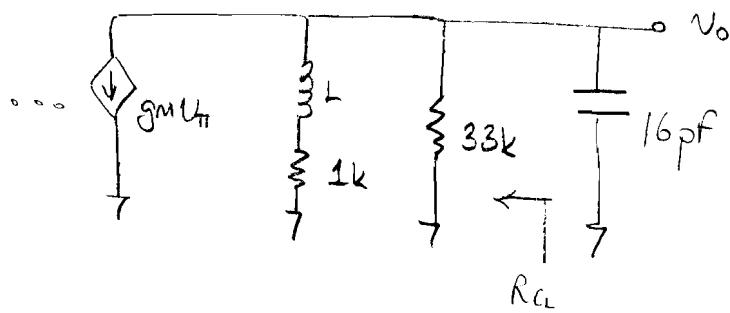
(1)

HW#3 Solutions 2008

EE 411 / 511

1) a) The transistor is an ideal NPN $\Rightarrow C_{be} = 0, C_{bc} = 0$

Since C_1, C_2 and C_3 are very large they do not affect the upper cutoff frequency and will be short circuited as mentioned in Open-Circuit time constants method.



$$L = 0 \Rightarrow \tau_{CL} = 1k\Omega // 33k\Omega \\ \approx 1k\Omega$$

$$\tau_{CL} = R_{CL} \cdot C_L \\ = 16 \text{ ns}$$

$$\omega_{h,est} = \frac{1}{\tau_{CL}} \Rightarrow \omega_{h,est} = \frac{1}{16 \times 10^{-9}} = 62,5 \times 10^6 \text{ rad/sec}$$

$$f_{h,est} \approx 9,95 \text{ MHz}$$

b) In order to extend the upper cutoff frequency shunt peaking technique is utilized for this amplifier.

The impedance of the RLC network can be written as:

$$Z(s) = (sL + 1k) // \frac{1}{sC} // 33k \approx (sL + 1k) // \frac{1}{sC}$$

↑
assuming that 33k is large compared to $(sL + 1k)$

Now, as explained in Section 8.2.1 of the course book, an expression for $\frac{\omega_{h,new}}{\omega_{h,old}}$ can be derived from this impedance $Z(s)$:

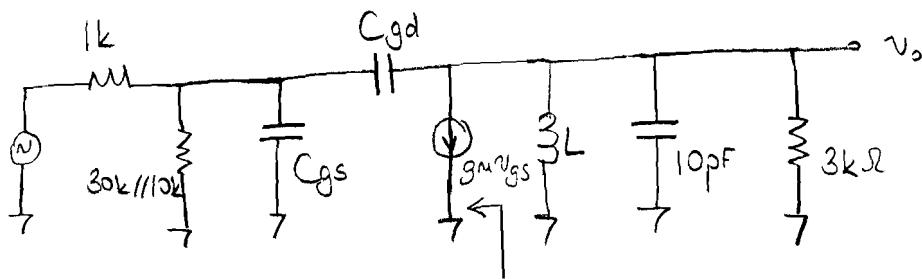
$$\frac{\omega_{h,new}}{\omega_{h,old}} = \sqrt{\left(-\frac{m^2}{2} + M + 1\right)} + \sqrt{\left(-\frac{m^2}{2} + M + 1\right)^2 + M^2} , \text{ where } M = \frac{RC}{L/R} , R = 1k\Omega$$

Then, maximum -3dB frequency ($\omega_{h,new}$) is found as 1,85 times $\omega_{h,old}$, when $M = \sqrt{2}$.

$$\Rightarrow M = \frac{RC}{L/R} = 1,41 = \frac{10^3 \cdot 16 \times 10^{-12}}{L/10^3} \Rightarrow L = 11,3 \mu\text{H}$$

(2)

2) a)



$$2\pi f_0 = \frac{1}{\sqrt{L C_{\text{TOTAL}}}}$$

$$\Rightarrow L = \frac{1}{C_{\text{TOTAL}} \times (2\pi f_0)^2}$$

$$= \frac{1}{11,86 \cdot 10^{-12} \cdot (2\pi \cdot 50 \cdot 10^6)^2}$$

$$\Rightarrow L = \boxed{0,854 \mu\text{H}}$$

If we use a test voltage source to find the admittance seen by the tank circuit, we end up with an equivalent resistance in parallel with an equivalent capacitance:

$$\begin{aligned} C_{\text{eq}} &= C_{\text{gd}} [1 + g_m (\overbrace{1k // 10k // 30k}^{882 \Omega})] \\ &= 0,1 \cdot 10^{-12} [1 + 20 \cdot 10^{-3} \cdot 882] \\ &= 1,86 \text{ pF} \end{aligned}$$

$$\Rightarrow C_{\text{TOTAL}} = 11,86 \text{ pF}$$

b)

$$C_{\text{input}} = C_{\text{gs}} + C_{\text{gd}} (1 + g_m R_L) \quad * \text{ See lecture notes (Tuned Amplifiers)}$$

$$= 0,2 \cdot 10^{-12} + 0,1 \cdot 10^{-12} (1 + 20 \cdot 10^{-3} \cdot 3 \cdot 10^3)$$

$$= \boxed{6,3 \text{ pF}}$$

* Assuming C_{in} is large compared to transistor capacitances. (not included)

c)

$$R_{\text{cin}} = 1k // 10k // 30k = 882 \Omega$$

Using Open Circuit time constants estimation:

$$\omega_{h,\text{est}} = \frac{1}{\tau_{\text{cin}}} = \frac{1}{R_{\text{cin}} C_{\text{in}}} = \frac{1}{882 \cdot 6,3 \cdot 10^{-12}} = 1,8 \cdot 10^8 \text{ rad/sec}$$

$$\Rightarrow f_{h,\text{est}} = \boxed{28,6 \text{ MHz}}$$