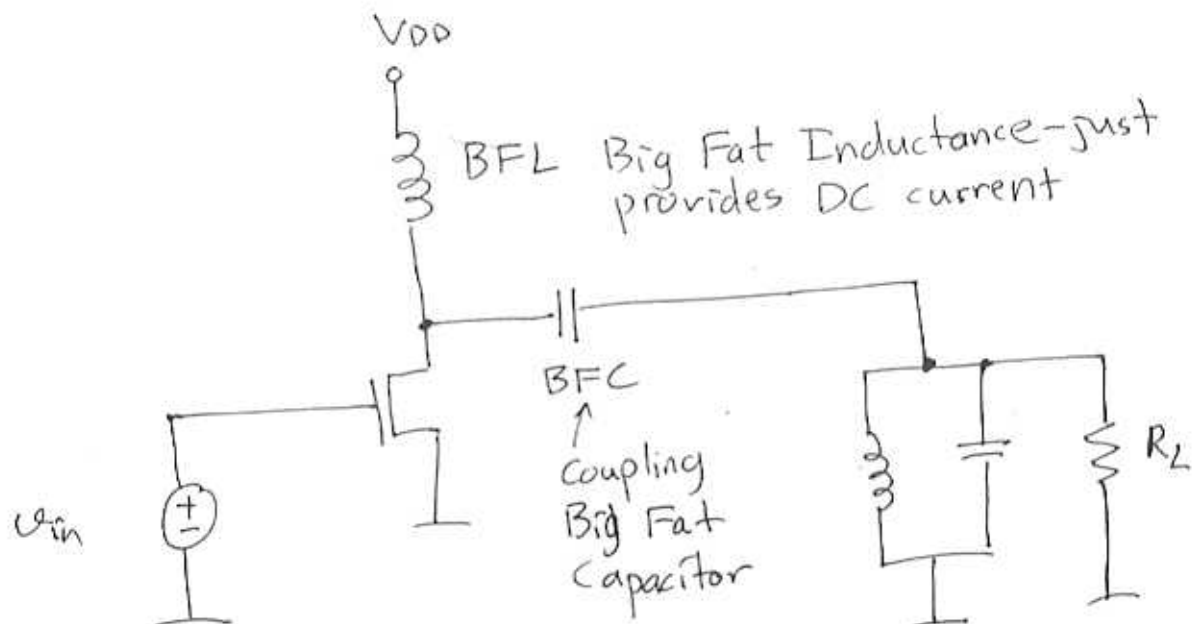


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(1)

RF POWER AMPLIFIERS

- Short talk on linearity and efficiency

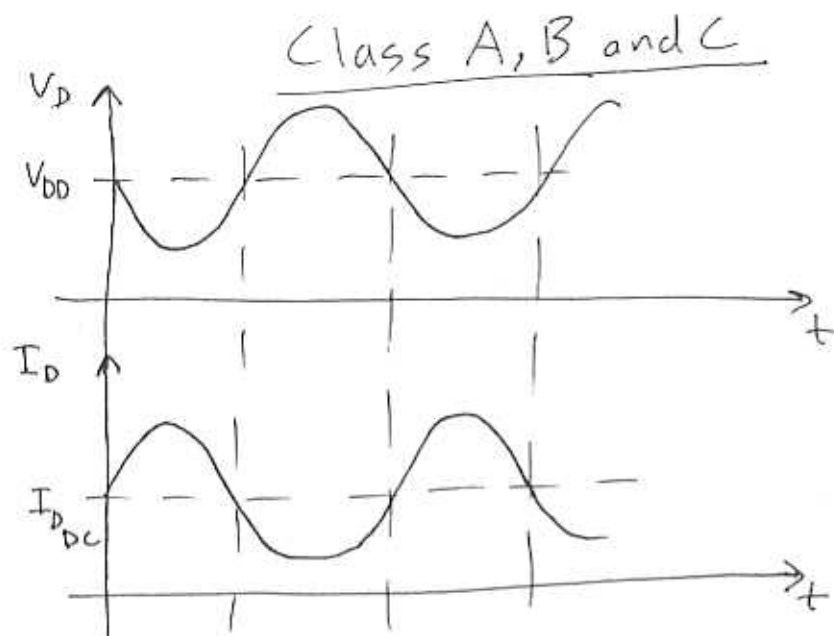


General Power Amplifier Model

Tank Q is high enough to create sinusoidal output voltage

Narrowband power amplifier

BJT $\rightarrow$ cut-off and saturation is avoided (Linear region)
MOS $\rightarrow$ always in saturation (pentode region) i.e.
the drain current does not depend on the drain voltage, but it is almost solely controlled by V_{gs} .

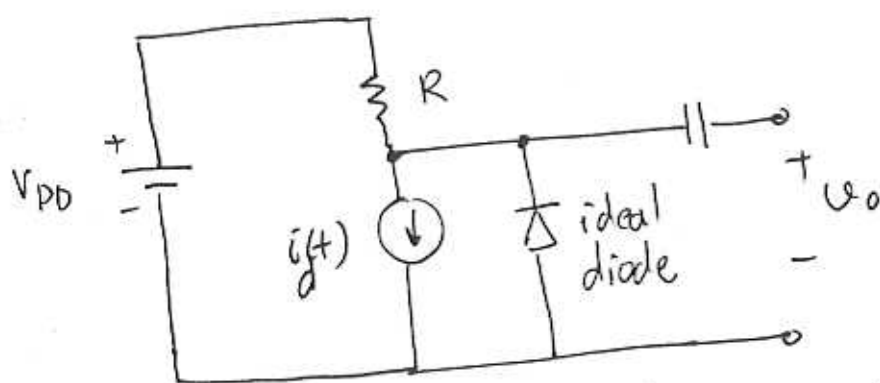


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(2)

Class-A amplifier resembles the small signal linear ^{tuned} amplifier with resistive Load R_L in many respects. If the current drive I_D is assumed to be sinusoidal (it does not necessarily be) The figures are representative.

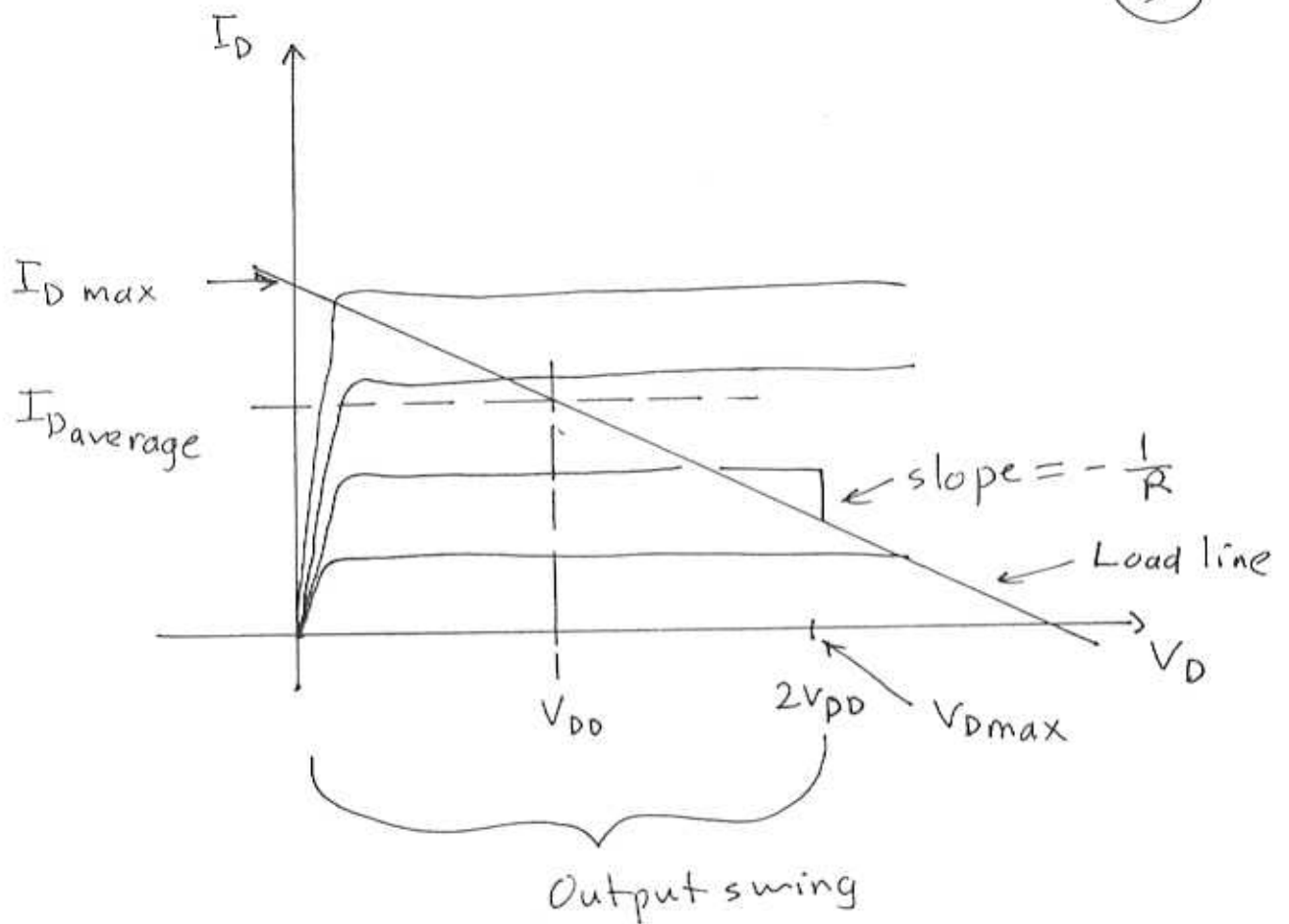
The circuit can be represented by :



where $i_d(t)$ is always positive

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3



Typical RF Power Amplifier load line for Class A amplifier

At all of the ^{Class} A, B and C amplifiers

$V_D = V_{DD} + V_{\text{tank}}$, therefore V_D is limited by $2V_{DD}$ since the peak tank voltage is limited by the drain saturation voltage.

$I_{D \text{ average}}$ depends on the Class of the amplifier

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(4)

$$I_D = I_{DC} + \hat{i}_{RF} \sin \omega_0 t \quad \text{if } I_D \geq 0$$
$$= 0 \quad \text{if } I_D < 0$$

$$I_{DC} > \hat{i}_{RF} \Rightarrow \text{Class A} \quad (\text{no distortion})$$

$$0 < I_{DC} < \hat{i}_{RF} \Rightarrow \text{Class AB} \quad (\text{Transistor starts switching})$$

$$I_{DC} = 0 \Rightarrow \text{Class B} \quad (\text{Transistor conducts at positive half cycle})$$

$$I_{DC} < 0 \Rightarrow \text{Class C} \quad (\text{Transistor conducts for a portion of the positive half cycle})$$

$$\text{At Class A} \quad I_{D \text{ average}} = I_{DC}$$

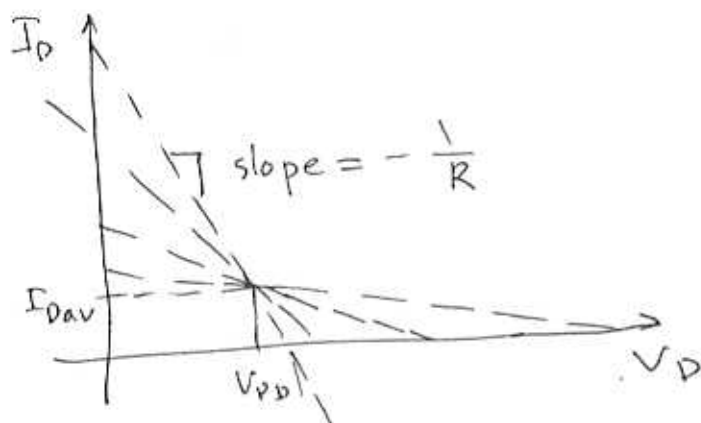
$$\text{At Class B} \quad I_{D \text{ average}} = \frac{\hat{i}_{RF}}{\pi}$$

$$\text{At Class C} \quad I_{D \text{ average}} = \frac{\hat{i}_{RF}}{\pi} (\sin \varphi - \varphi \cos \varphi)$$

where φ is the conduction angle of the input current.

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(5)

Class A amplifier

Assuming that the linear region is limited by either $V_D = 0$ or $I_D = 0$

The always correct equation for power output

$$P_{RF} = \frac{I_{peak}^2 R}{2} = \frac{(I_{fundamental})^2 R}{2}$$

and the DC power supplied by the power supply

$$P_{DC} = V_{DD} \times I_{D \text{ average}} \text{ (constant for class A ampl.)}$$

The voltage swing over the transistor is always symmetrical and sinusoidal because of the output tank circuit.

The maximum peak swing which is limited on the lower side by $V_D = 0$, dictates a maximum fundamental drain swing $= 2V_{DD}$ or $V_p = V_{DD}$

The current drive at the lowest possible level is limited by $I_D = 0$, therefore

$$I_{peak} = I_{DC} \text{ for maximum current drive}$$

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(6)

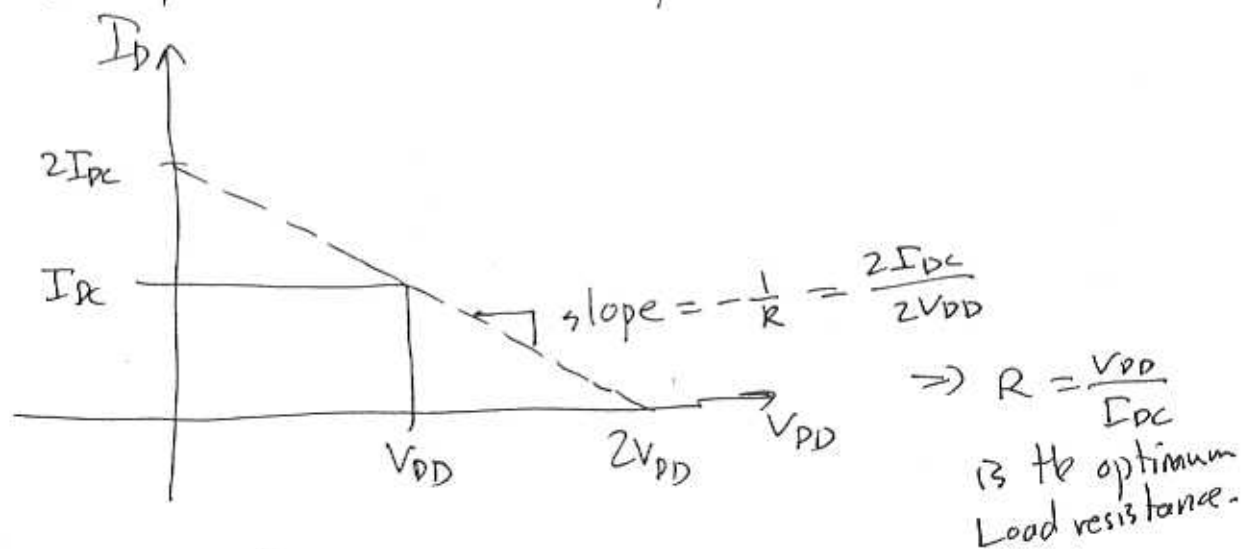
In that case the maximum power output which can be drawn from the circuit is

$$P_{RF} = \frac{I_P \times V_P}{2} = \frac{I_{DC} \times V_{DD}}{2}$$

Therefore the maximum efficiency is

$$\eta_{max} = \frac{(I_{DC} \times V_{DD})}{2} \cdot \frac{1}{I_{DC} V_{DD}} = \frac{1}{2}$$

Therefore at the maximum power output



for $\eta = \frac{1}{2}$

The normalized power output capability for Class A amplifier, which is in general defined by:

$$\frac{P_{Omax}}{V_{max} \times I_{max}} = \frac{I_{DC} V_{DD} / 2}{2I_{DC} \times 2V_{DD}} = \frac{1}{8}$$

Class B amplifier

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(7)

$$i_D = i_{RF} \sin \omega t \quad \text{for } i_D > 0$$

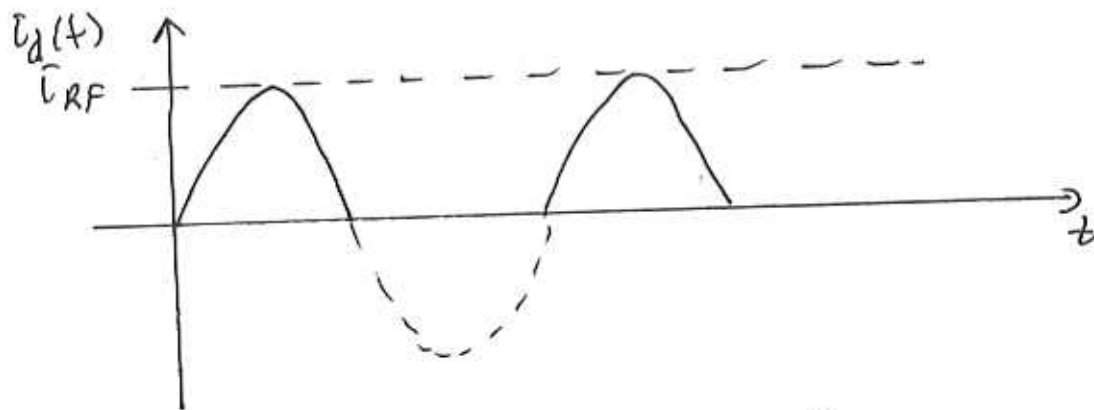
$$i_D = 0 \quad \text{for } i_D \leq 0$$

$$i_{fund} = \frac{i_{RF}}{2} \Rightarrow v_{out} = \frac{i_{RF}}{2} R \sin \omega t$$

$$i_{fund} = \frac{2}{T} \int_0^{T/2} i_{RF} \sin \omega t \sin \omega t dt = i_{RF}/2$$

$$P_{out} = \left(v_{out_p} \times \frac{i_{RF}}{2} \right) \frac{1}{2} = \frac{i_{RF} \times R/2 \times i_{RF}/2}{2} = \frac{i_{RF}^2 R}{8}$$

for any given amplitude of i_{RF}



The average current drawn from the power supply is

$$\frac{1}{\pi} i_{RF} \Rightarrow P_{DC} = \frac{V_{DD} \cdot i_{RF}}{\pi} \Rightarrow$$

The efficiency of the class B amplifier at any given drive level i_{RF} is given by

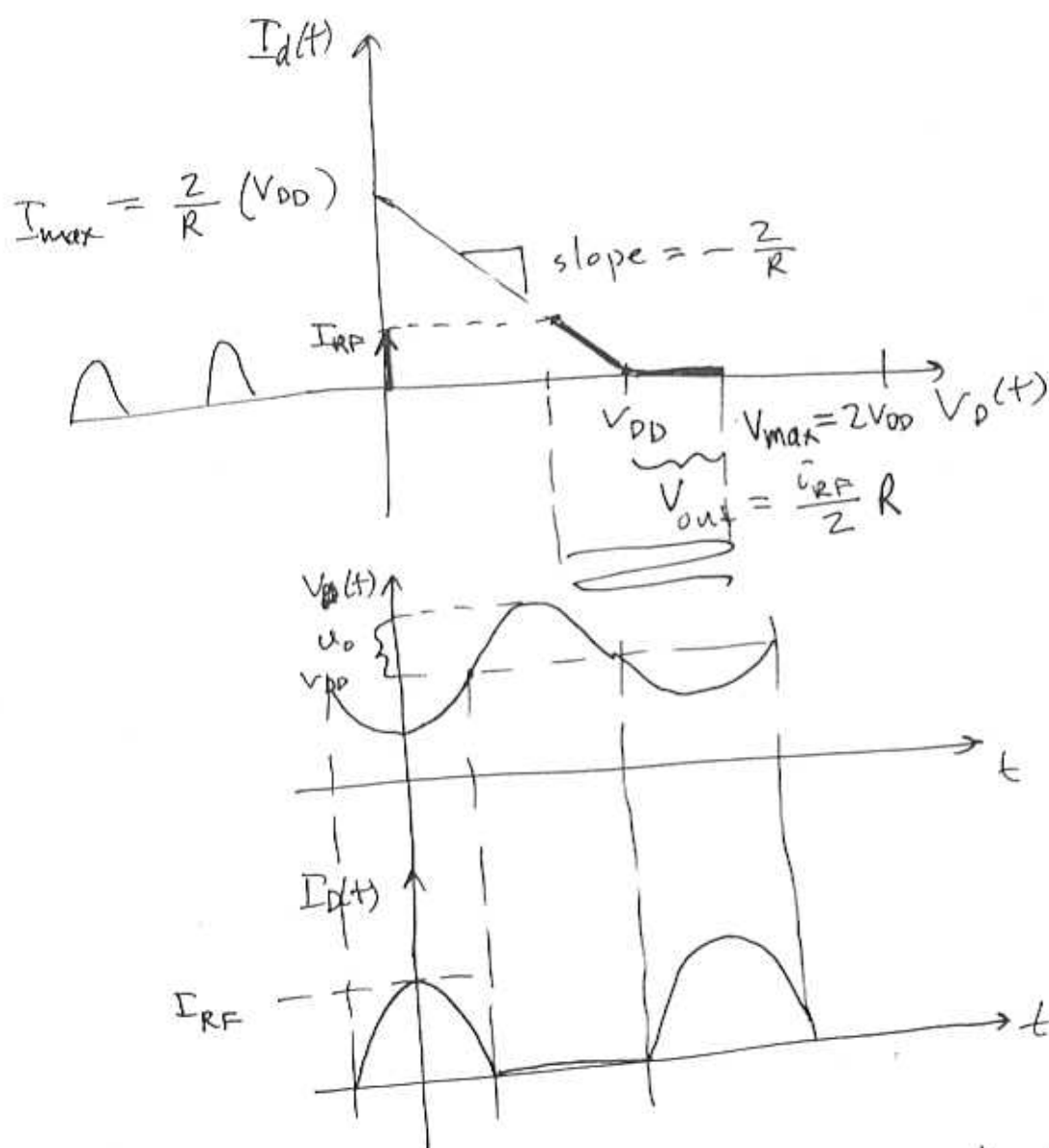
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(7-A)

$$\eta = \frac{P_{RF}}{P_{DC}} = \frac{\frac{\hat{I}_{RF}^2 R}{8}}{\frac{V_{DC} \hat{I}_{RF}}{\pi}} = \frac{\hat{I}_{RF} R \pi}{8 V_{DC}} = 0.393 \frac{\hat{I}_{RF} R}{V_{DC}}$$

In order to see the load curve (in contrast to load line of $V_D(t)$ versus $I_D(t)$) of $V_D(t)$ and $I_D(t)$, let us carry out a short analysis:

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(8)



At any nonlinear current drive with tuned load (parallel) the output swing is determined by the fundamental current and the fundamental voltage

Small signal drive case at Class B ampl.

$$V_{out}(t) = -R \bar{i}_{fund} \cos \omega_0 t$$

$$= -R \frac{I_{RF}}{2} \cos \omega_0 t$$

$$I_D(t) = I_{RF}(t) = \bar{I}_{RF} \cos \omega_0 t \quad \text{for } I_D(t) \geq 0$$

$$= 0 \quad \text{if } I_D(t) < 0$$

for $\pm \pi/2$ period $I_D(t) = I_{RF} \sin \omega_0 t$ (1)

$$V_D(t) = V_{DD} - R \bar{i}_{fund}(t) = V_{DD} - R \frac{I_{RF} \sin \omega_0 t}{2}$$

(2)

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(9)

Solving for $V_D(t)$ in terms of $I_D(t)$ from equations (1) & (2)

$$V_D(t) = V_{DD} - R \frac{I_{RF}}{2} \cdot \frac{I_D(t)}{I_{RF}}$$

$$V_D(t) = V_{DD} - \frac{R}{2} I_D(t) \quad \text{or}$$

$$I_D(t) = \frac{2}{R} [V_{DD} - V_D(t)]$$

The load curve, i.e. the loci of the all possible $V_D(t)$ & $i_D(t)$ pairs is two straight lines extending from the point $V_D(t) = V_{DD}$ and $i_D(t) = 0$ to $V_D(t) = 2V_{DD}$ & $i_D(t) = 0$ and $V_D(t) = 0$ & $i_D(t) = I_{ma}$.

The span covered is shown as the thick line depending on the drive level. At maximum drive level, which corresponds to maximum efficiency, the current spans 0 to $\frac{2}{R} V_{DD}$ and the voltage spans 0 to $2V_{DD}$ where $i_{RF} = \frac{2V_{DD}}{R}$ & $V_{DC} = V_{DD} \Rightarrow$

$$\eta_{\max} = 0.393 \frac{2V_{DD}}{R} \cdot \frac{R}{V_{DD}} = \frac{\pi}{8} \cdot 2 = \frac{\pi}{4} = 0.785$$

($\eta_{\max}$ corresponds to the maximum possible i_{RF} since it is proportional with it.)

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9-A

The normalized power output capability which is defined as

$$\begin{aligned}\frac{P_{o\max}}{V_{\max} \times I_{\max}} &= \frac{I_{\text{fund}\max}^2 R / 2}{V_{\max} I_{\max}} = \frac{\left(\frac{I_{\text{RF}\max}}{2}\right)^2 \cdot \frac{R}{2}}{V_{\max} \times I_{\max}} \\ &= \frac{\left(\frac{I_{\max}}{2}\right)^2 \cdot \frac{R}{2}}{V_{\max} \times I_{\max}} = \frac{\frac{I_{\max}^2}{4} \cdot \frac{1}{2} \cdot \frac{V_{\max}}{I_{\max}}}{V_{\max} \times I_{\max}} = \frac{1}{8}\end{aligned}$$

Where R_{opt} can be found from

$$I_{\max} = \frac{2}{R_{\text{opt}}} V_{\text{DD}} = \frac{2}{R_{\text{opt}}} \cdot \frac{V_{\max}}{2} \Rightarrow$$

$$R_{\text{opt}} = \frac{V_{\max}}{I_{\max}}$$

for class B amplifiers

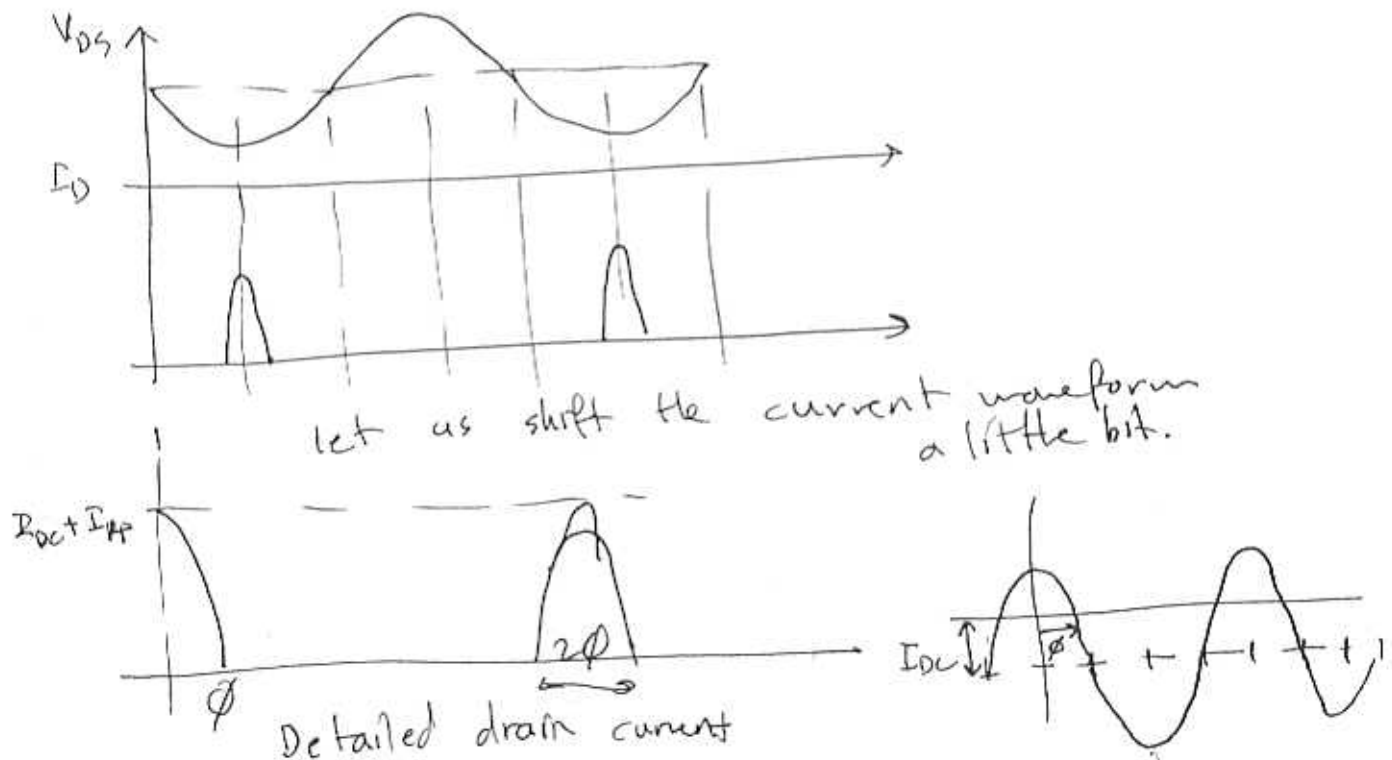
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10

Class C amplifier

The transistor conducts for less than the half period

$$\begin{aligned} i_D &= I_{DC} + \bar{i}_{RF} \sin \omega t, \quad i_D > 0 \\ i_D &= 0 \quad \text{for the rest of the period.} \end{aligned}$$



$$\text{let } i_D = I_{DC} + \bar{i}_{RF} \cos \omega t, \quad i_D > 0$$

$$2\phi = 2 \cdot \cos^{-1} \left(-\frac{I_{DC}}{\bar{i}_{RF}} \right)$$

$$-I_{DC} = \bar{i}_{RF} \cos \phi$$

$$\bar{i}_D = \frac{1}{2\pi} \int_{-\phi}^{+\phi} (I_{DC} + \bar{i}_{RF} \cos \theta) d\theta = \frac{1}{2\pi} 2\phi I_{DC} + \frac{\bar{i}_{RF}}{2\pi} \sin \theta \Big|_{-\phi}^{+\phi}$$

$$\bar{i}_D = \frac{\bar{i}_{RF}}{\pi} (\sin \phi - \phi \cos \phi)$$

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(11)

$$\bar{i}_{\text{fund}} = \frac{1}{\pi} \int_{-\varphi}^{+\varphi} (I_{\text{DC}} + \bar{i}_{\text{RF}} \cos \theta) d\theta \cos \theta$$

$$= \frac{2}{\pi} \int_0^{\varphi} (I_{\text{DC}} + \bar{i}_{\text{RF}} \cos \theta) d\theta \cos \theta$$

$$\bar{i}_{\text{fund}} = \frac{2}{\pi} \left\{ I_{\text{DC}} \sin \theta \Big|_0^{\varphi} + \frac{\bar{i}_{\text{RF}}}{2} \int_0^{\varphi} [1 + \cos 2\theta] d\theta \right\}$$

$$= \frac{2}{\pi} \left\{ I_{\text{DC}} \sin \varphi + \frac{\bar{i}_{\text{RF}} \varphi}{2} + \frac{\bar{i}_{\text{RF}}}{2} \frac{\sin 2\varphi}{2} \right\}$$

$$= \frac{2}{\pi} \left(-\bar{i}_{\text{RF}} \cos \varphi \sin \varphi + \frac{\bar{i}_{\text{RF}} \varphi}{2} + \frac{\bar{i}_{\text{RF}} \sin 2\varphi}{4} \right)$$

$$\cos \varphi \sin \varphi = \frac{\sin 2\varphi}{2}$$

$$= \frac{2 \bar{i}_{\text{RF}}}{\pi} \left[-\frac{\sin 2\varphi}{4} + \frac{\varphi}{2} + \frac{\sin 2\varphi}{4} \right]$$

$$= \frac{2 \bar{i}_{\text{RF}}}{\pi} \left[\frac{\varphi}{2} - \frac{\sin 2\varphi}{4} \right] = \frac{\bar{i}_{\text{RF}}}{\pi} \left[\varphi - \frac{\sin 2\varphi}{2} \right]$$

$$V_{\text{out peak}} = \frac{\bar{i}_{\text{RF}}}{2\pi} [2\varphi - \sin 2\varphi] R$$

$$\eta = \frac{P_{out}}{P_{in}} = \frac{\frac{\bar{i}_{fund}^2 R}{2}}{V_{DD} \cdot \bar{i}_D}$$

$$= \frac{\left(\frac{\bar{i}_{RF}}{2\pi} [2\phi - \sin 2\phi] R \frac{\bar{i}_{RF}}{2\pi} [2\phi - \sin 2\phi] \right)}{2 \times V_{DD} \underbrace{\frac{\bar{i}_{RF}}{\pi} [\sin \phi - \phi \cos \phi]}_{\text{average current}}}$$

at the maximum efficiency point
 $V_{peak} = V_{DD}$

$$\eta_{max} = \frac{\frac{\bar{i}_{RF}}{2\pi} [2\phi - 2\sin \phi] \times V_{DD}}{2 \times V_{DD} \frac{\bar{i}_{RF}}{\pi} [\sin \phi - \phi \cos \phi]}$$

$$\eta_{max} = \frac{[2\phi - 2\sin \phi]}{4 [\sin \phi - \phi \cos \phi]}$$

ϕ (rad)	η_{max}
0.01	0.99999034
0.1	0.999
0.2	0.996
0.3	0.991
0.4	0.98
0.5	0.975
1.0	0.9
$\pi/2$	0.7854

assuming that
 R is chosen such that
 $V_{peak} = V_{DD}$ each time

Talk about
switching loss

in practice these values can not even be approached
 $\rightarrow$ class B amplifier

$$V_D(t) = V_{DD} - R \bar{i}_f(t)$$

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(13)

$$V_D(t) = V_{DD} - R k \bar{i}_{RF} \cos \omega t$$

$$= V_{DD} - kR [I_D(t) - I_{DC}]$$

$$= V_{DD} - kR I_D(t) + kR I_{DC}$$

$$I_D(t) = I_{DC} + \bar{i}_{RF} \cos \omega t$$

$$\bar{i}_{RF} \cos \omega t = I_D(t) - I_{DC}$$

where

$$k = \frac{1}{2\pi} [2\phi - \sin 2\phi]$$

$$= \frac{1}{\pi} \left[\phi - \frac{\sin 2\phi}{2} \right]$$

$$kR I_D(t) = V_{DD} - V_D(t) + kR I_{DC}$$

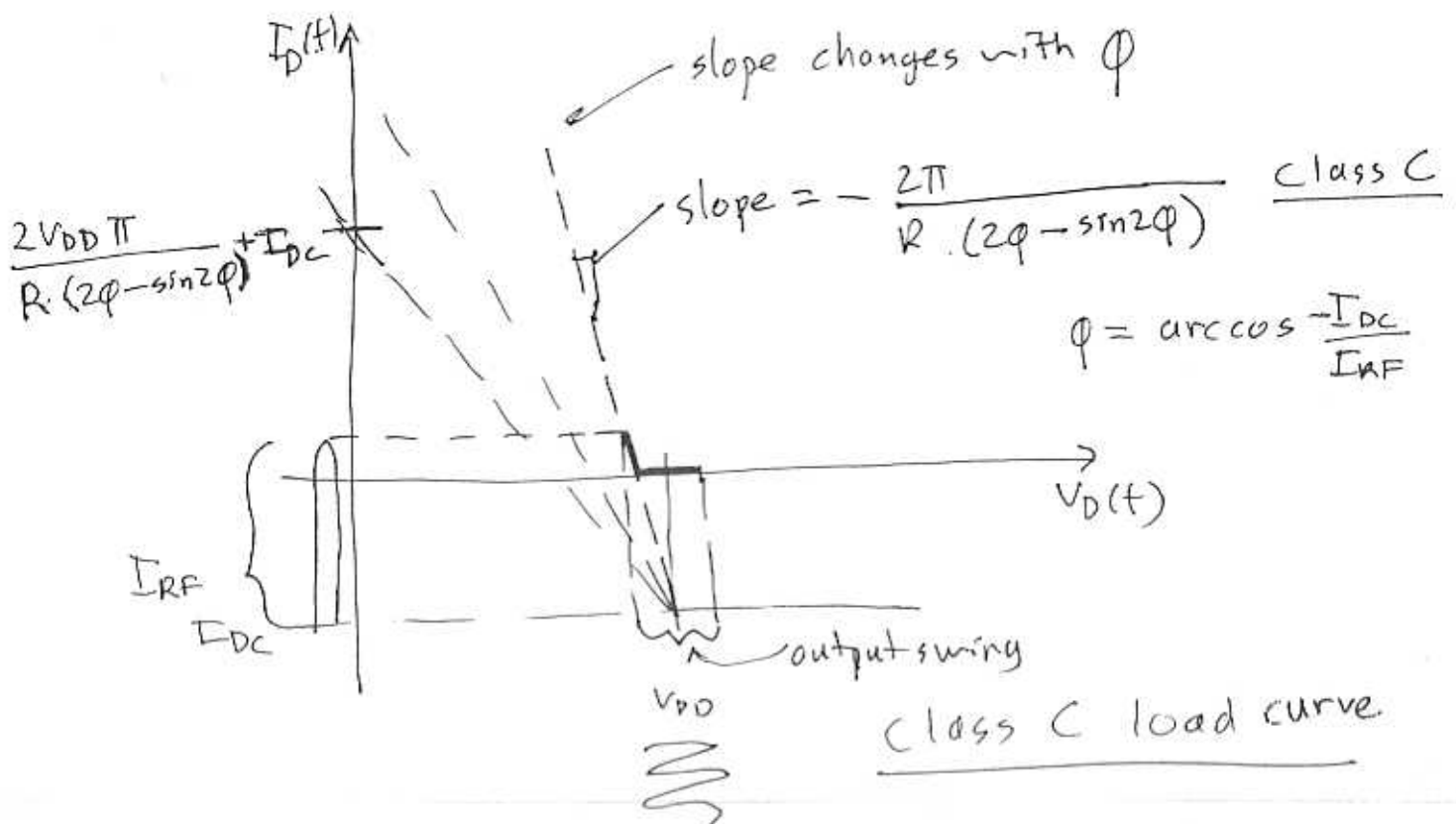
$$= [V_{DD} + kR I_{DC}] - V_D(t)$$

$$I_D(t) = \frac{V_{DD}}{kR} + I_{DC} - \frac{V_D(t)}{kR}$$

$$I_D(t) = \left[\frac{V_{DD} - V_D(t)}{R} \right] \frac{\pi}{\phi - \frac{\sin 2\phi}{2}} + I_{DC}$$

always > 2

where ϕ is the single sided conduction angle



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13-A

As can be observed from the figure for arbitrary selection of R and ϕ and I_{DC} the instantaneous $v_d(t)$ & $i_d(t)$ locus can assume lines with different slopes. But for maximum efficiency & maximum output, the output voltage swing must cover $V_D(t)$ range from 0 to $2V_{PD}$ and current must reach to I_{max} where the load line intersects with the current axis and this point is defined by

$$I_{max} = I_{DC} + \frac{2V_{PD}\pi}{R(2\phi - \sin 2\phi)} \quad \text{equ(1)}$$

At such a configuration I_{max} , I_{DC} , I_{RF} & ϕ become related to each other as

$$\cos \phi = \frac{-I_{DC}}{I_{RF}} = \frac{-I_{DC}}{I_{max} - I_{DC}} \Rightarrow$$

$$I_{max} \cos \phi - I_{DC} \cos \phi = -I_{DC} \Rightarrow$$

$$I_{max} \cos \phi = I_{DC} (\cos \phi - 1) \Rightarrow$$

$$I_{DC} = I_{max} \frac{\cos \phi}{\cos \phi - 1}$$

Now rearranging equation 1 to express $R_{optimum}$

$$R_{opt} = \frac{V_{max}}{I_{max} - I_{DC}} \cdot \frac{\pi}{2\phi - \sin 2\phi} \quad \text{where } V_{max} = 2V_{PD}$$

and substituting I_{DC} , R_{opt} becomes

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(13-B)

$$R_{opt} = \frac{V_{max}}{I_{max} \left[1 - \frac{\cos \phi}{\cos \phi - 1} \right]} \cdot \frac{\pi}{2\phi - \sin \phi}$$

$$= \frac{V_{max}}{I_{max}} \cdot \frac{\pi}{\left[\frac{\cancel{\cos \phi} - 1 - \cancel{\cos \phi}}{\cos \phi - 1} \right]} \cdot \frac{1}{2\phi - \sin \phi}$$

$$= \frac{V_{max}}{I_{max}} \cdot \frac{\pi (1 - \cos \phi)}{2\phi - \sin \phi} \quad \text{where } \phi = \arccos \frac{I_{DC}}{I_{DC} - I_{max}}$$

$$R_{opt} = \frac{V_{max}}{I_{max}} \cdot \frac{\pi (1 - \cos \phi)}{2\phi - \sin 2\phi} \quad (\text{equ 2})$$

which is the expression for R_{opt} in terms of V_{max} , I_{max} and ϕ which is half the conduction angle. The conduction angle can also be expressed as

$$\cos \phi = \frac{I_{DC}}{I_{DC} - I_{max}} \quad \text{or as in the equation stated}$$

a while ago as

$$I_{DC} = I_{max} \frac{\cos \phi}{\cos \phi - 1} \quad (\text{equ 3})$$

Therefore if a transistor with a set of V_{max} & I_{max} is given or if a set of values I_{max} & V_{max} are given as the required drive levels then for an amplifier with optimum efficiency and maximum output with those levels,

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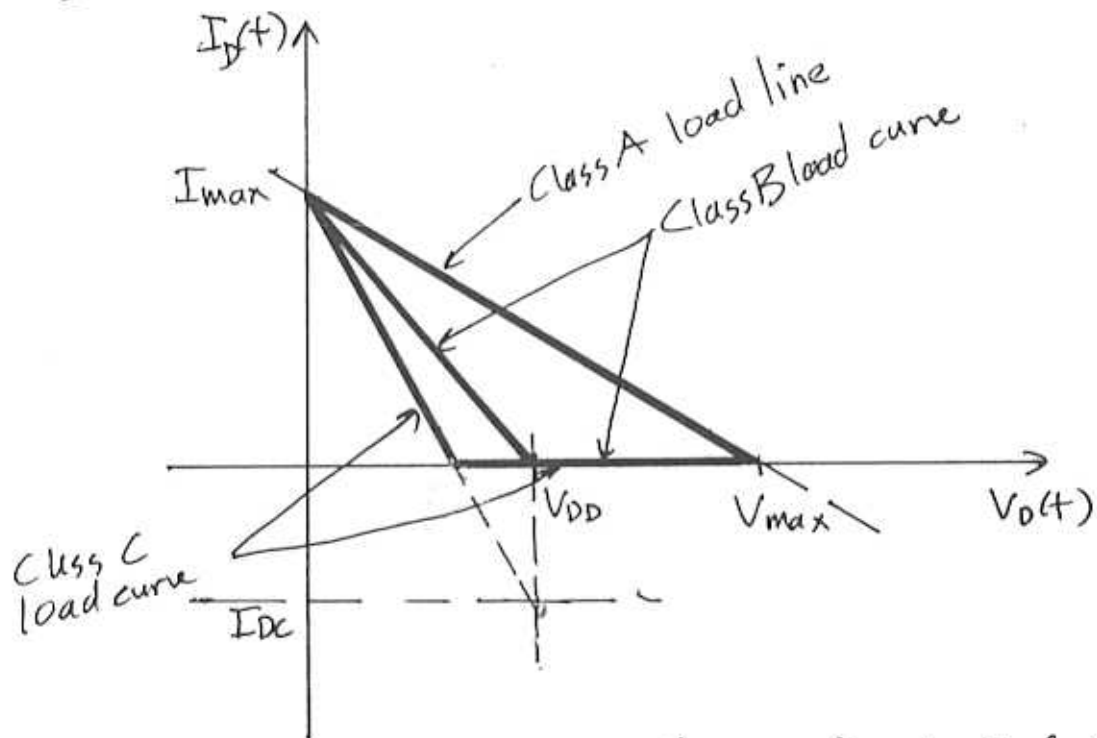
13-C

then, R_{opt} & I_{dc} must be selected using (equ 2) & (equ 3) after selecting the conduction angle 2ϕ .

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(14)

Summary of Class A, B & C type amplifiers for optimum solutions



Optimum load lines (curves) A, B & C
class of amplifiers

$$R_{opt} = \frac{V_{max}}{I_{max}} = \frac{V_{DD}}{I_{fund}} = \frac{V_{DD}}{I_{RF}} \text{ for Class A}$$

$$R_{opt} = \frac{V_{max}}{I_{max}} = \frac{V_{max}}{I_{RF}} = \frac{2V_{DD}}{I_{RF}} = \frac{2V_{DD}}{2I_{fund}} \text{ for Class B}$$

$$R_{opt} = \frac{V_{max}}{I_{max}} \cdot \frac{\pi(1-\cos\phi)}{2\phi - \sin 2\phi} = \frac{2V_{DD}}{I_{RF} + I_{DC}} \cdot \frac{\pi(1-\cos\phi)}{(2\phi - \sin 2\phi)}$$

$$\text{where } \phi = \arccos \frac{-I_{DC}}{I_{RF}}$$

for Class C

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(14-A)

Or alternatively R_{opt} for Class C can be expressed as by substituting $\cos \phi = \frac{-I_{DC}}{I_{RF}}$

$$R_{optimum} = \frac{2V_{DD}}{I_{RF} + I_{DC}} \cdot \frac{\pi \left(1 - \frac{-I_{DC}}{I_{RF}}\right)}{2\phi - 2\sin \phi}$$

$$= \frac{2V_{DD}}{I_{RF} + I_{DC}} \cdot \frac{\pi \frac{I_{RF} + I_{DC}}{I_{RF}}}{2\phi - 2\sin \phi}$$

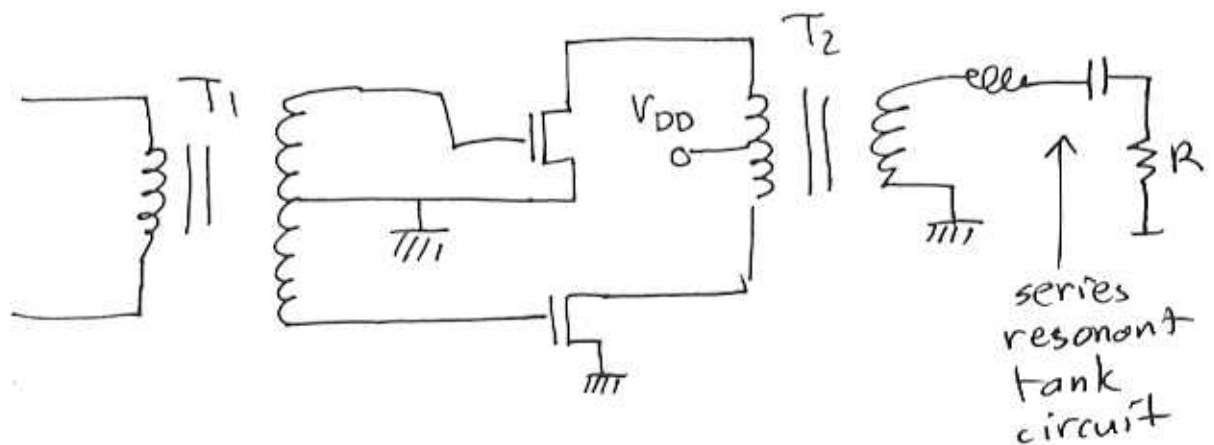
$$R_{opt} = \frac{V_{DD}}{I_{RF}} - \frac{2\pi}{(2\phi - 2\sin \phi)}$$

One can therefore also choose V_{DD} , I_{RF} and conduction angle 2ϕ and can then determine R_{opt} accordingly.

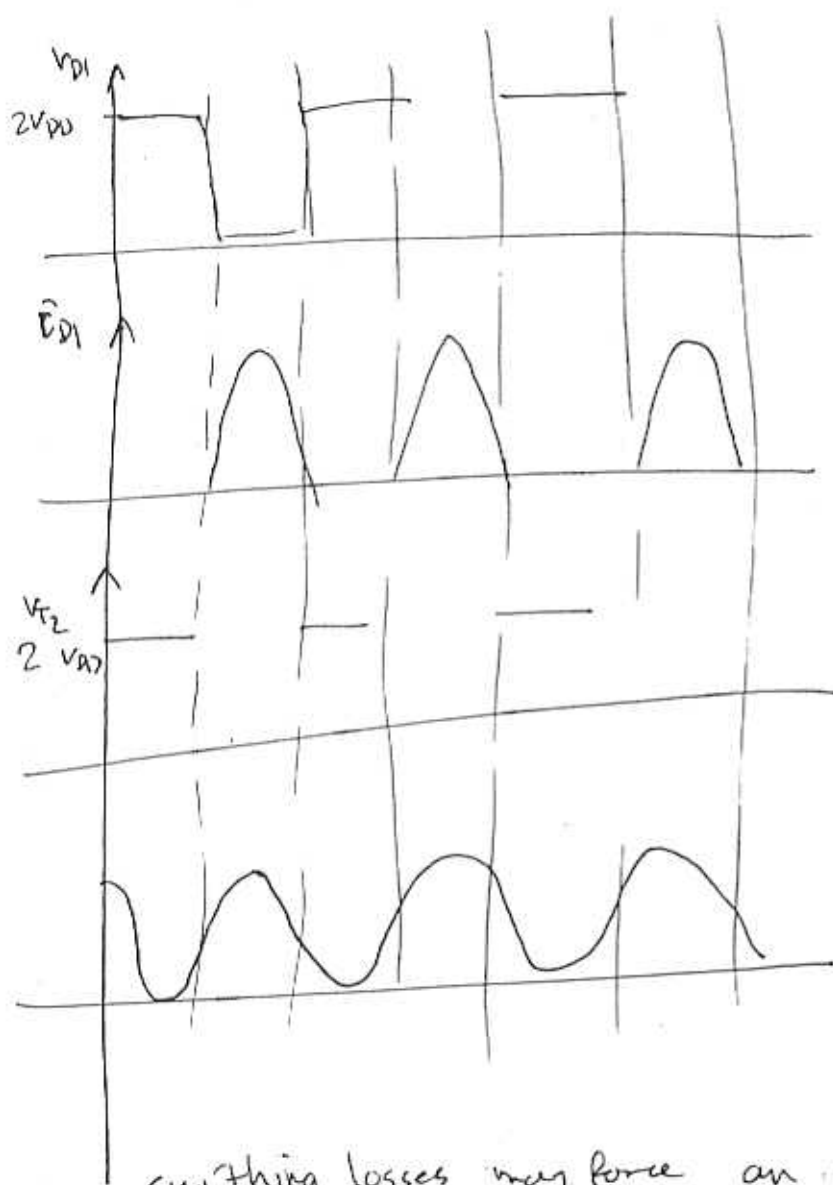
Class AB amplifier

Class AB amplifier is a driving method between class A & class B. Class AB is a somewhat linear amplifier with a smaller bias current so that at large drive levels the current drive is limited at the lower end.

Class D amplifier



The transistors act like switches, as in a push-pull amplifier, but they remain on for one half cycle and remain off for the other half cycle (alternately).



square wave voltage on primary and secondary $\Rightarrow$ therefore only switching losses

If you drive the transistors hard enough the transformer acting as inductor will saturate the transistor when at the active state. Once one transistor stays in sat for one half period, the other one will have to force the voltage on its drain. of V_{DD}

switching losses may force an efficiency down to 60-70%