

EEE 202 CIRCUIT THEORY

Spring 2011-12

Information Sheet

Instructor : Ömer Morgül

Office/Phone/e-mail : EE408/1529/morgul@ee.bilkent.edu.tr

Office Hours : Wed. 15:30 - 17:30.

TA's : E. Kopanoğlu (Coordinator), K. Güngör (Coordinator), S. Gökyar,
B. Ghassemiparvin, E. Sarı, H. Akçalı, E. Gönendik.

Class Time : Sec. 2 Mon. 15:40-17:30 (EE 517)
 Thu. 13:40-15:30 (EE 517)

Lab. Hours : Fri. 8:40-12:30, EE 211.

Text :

The Analysis and Design of Linear Circuits, R. E. Thomas, A. J. Rosa, G. J. Toussaint, 6th. ed., John Wiley, 2009. (Required.)

Additional References :

The Analysis and Design of Linear Circuits, R. E. Thomas, A. J. Rosa, 4th. ed., John Wiley, 2003. (Other editions are also OK.)

Electric Circuits, N. Balabanian, McGraw-Hill, 1994.

Linear and Nonlinear Circuits, L. O. Chua, C. A. Desoer and E. S. Kuh, McGraw-Hill, 1987.

Basic Circuit Theory, C. A. Desoer and E. S. Kuh, McGraw-Hill, 1969,(15th prt. 1985)

Introduction to Electric Circuits, R. C. Dorf, J. A. Svoboda, 5th ed., John Wiley, 2001.

Microelectronic Circuits, A. S. Sedra and K. C. Smith, Holt, Rinehart and Winston, 1987.

You can get additional references in the library. The related books are usually indexed by the call number TK454...

Other Materials : PSpice.

Grading : Interview : 10%, Lab : 20%, 2 Midterms : 20% (each), Final : 30%

Exam Schedule :	Midterm 1	Friday, March. 16
	Midterm 2	Friday, Apr. 20.
	Final	Not scheduled yet.

Course Policy :

1 : Students are required to check the course web site frequently for course related announcements. For course web page, see :www.ee.bilkent.edu.tr

2 : There will be 10 Labs done in EE 211 (Computer Room). Labs are based on PSpice (software). These experiments will be done individually. Interviews are also given during labs sessions.

Course Coverage

Chp. 1 : Introduction. Sections 2,3.

Chp. 2 : Basic Circuit Analysis. All, except section 7.

Chp. 3 : Circuit Analysis Techniques. All, except section 6.

Chp. 4 : Active Circuits. All.

Chp. 5 : Signal Waveforms. All except section 7.

Chp. 6 : Capacitance and Inductance. All.

Chp. 7 : First and Second Order Circuits. All.

Chp. 8 : Sinusoidal Steady State Response. All.

Chp. 16 : Power in The Sinusoidal Steady State. Sections 1, 2.

Chp. 9 : Laplace Transforms. All.

Chp. 10 : s-Domain Circuit Analysis. All.

Chp. 11 : Network Functions. All except section 7.

Chp. 12 : Frequency Response. All.

Chp. 13 - 15, 17 : Omit.